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Subclass	Class	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
AK SCANNED <u>SBP</u> Q.A. <u>RL</u>	

APPLICATION NO. 09/678175	CONT/PRIOR	CLASS 913 213	SUBCLASS 1	ART UNIT 2181 2825	EXAMINER Kissasinek
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ANTS Victor Konrad
Vivek Joshi

Method to reduce the power consumption of large plas by clock gating guided by recursive shannon decomposition of the and-plane

Title

PTO-2040
12/99

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
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	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
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	(Legal Instruments Examiner) _____ (Date)				

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